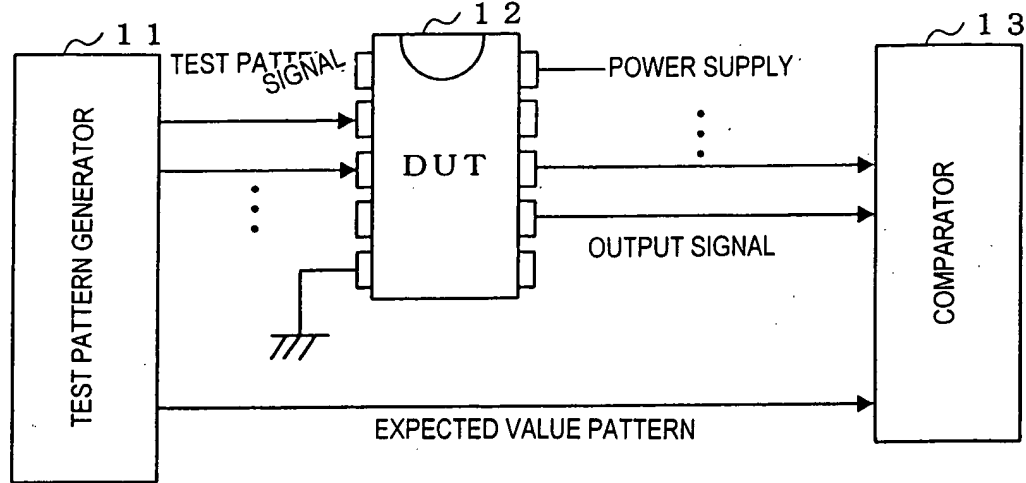


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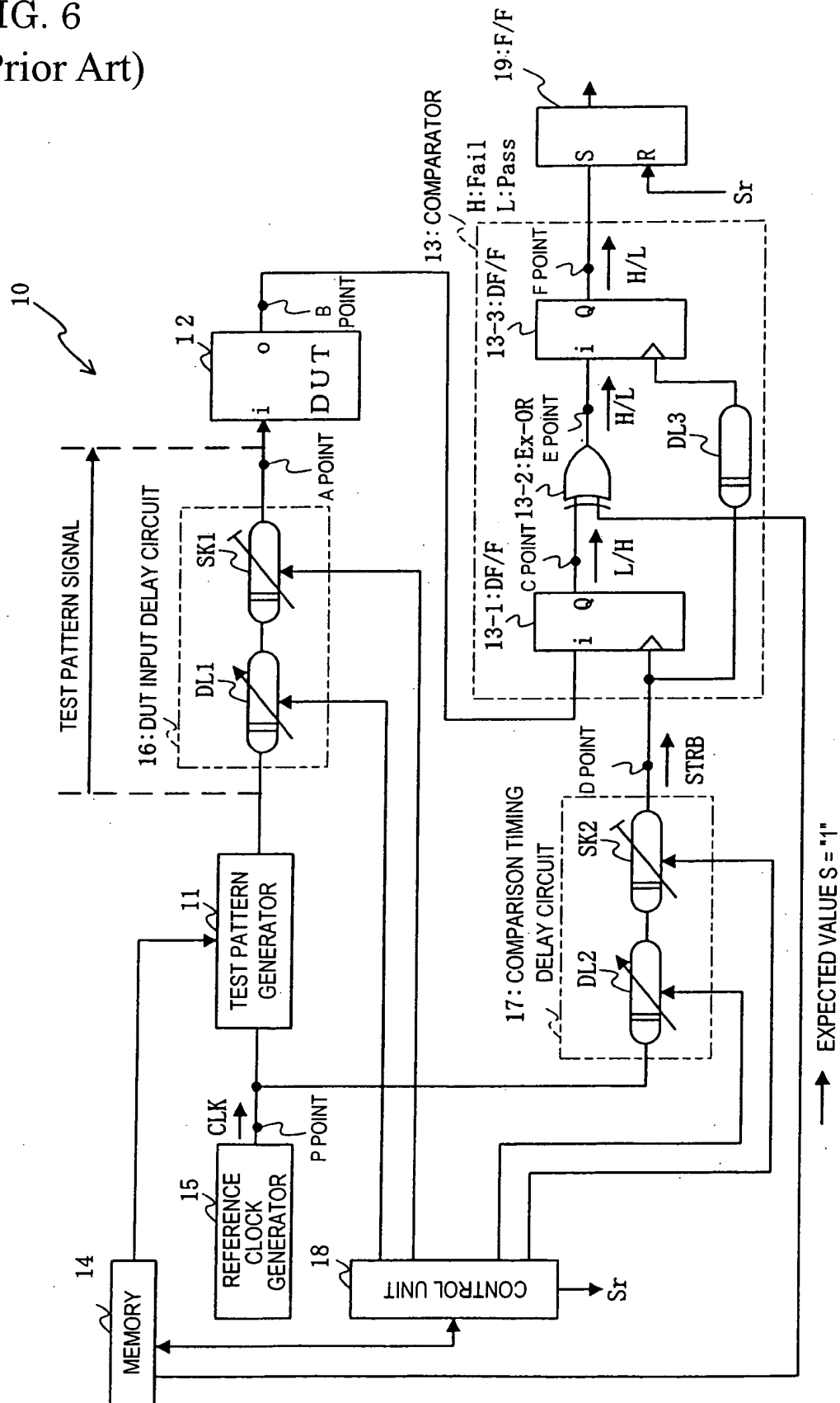
FIG. 5
(Prior Art)



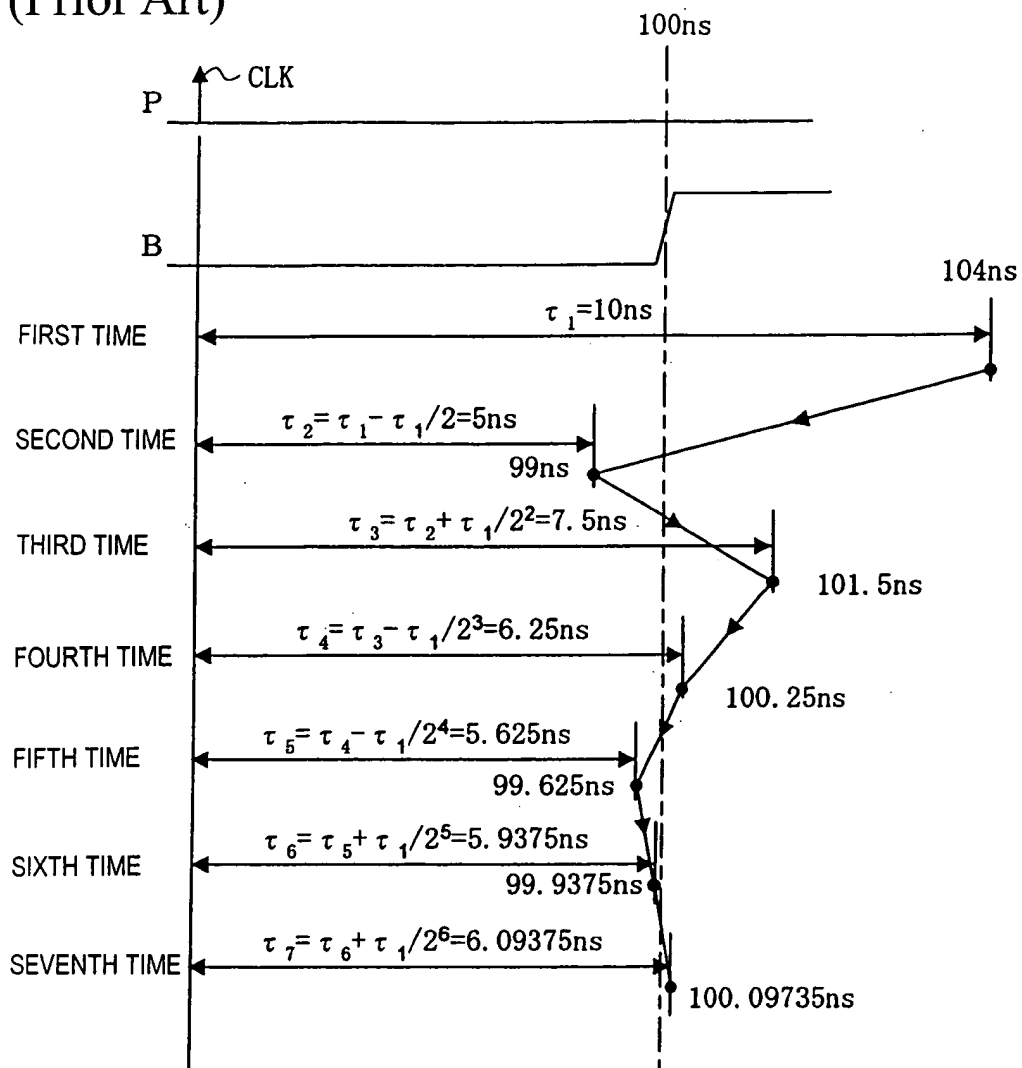
10 : SEMICONDUCTOR TEST APPARATUS

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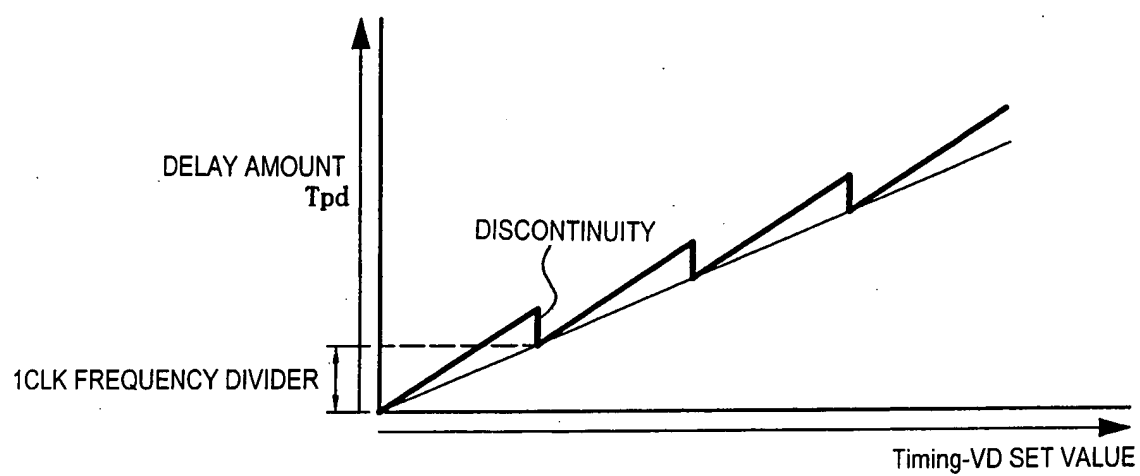
FIG. 6
(Prior Art)



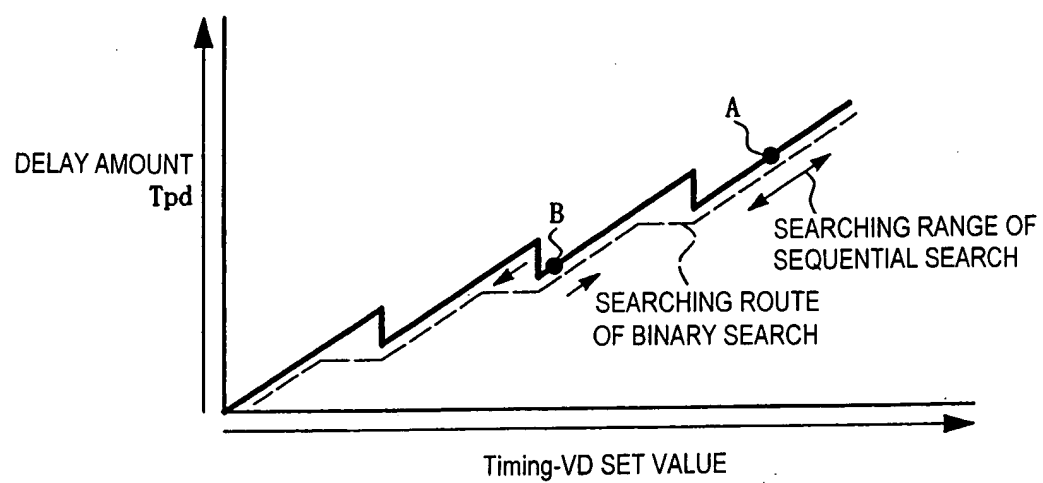
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FIG. 7
(Prior Art)

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FIG. 8
(Prior Art)

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FIG. 9
(Prior Art)

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FIG. 10
(Prior Art)